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Date: December 8, 2003

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MESSAGE:

Following find an Interview Summary which is being filed via facsimile this 8th day of December, 2003.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

below.

Applicant:

Yu

Title:

MOS TRANSISTOR WITH

ASYMMETRICAL

SOURCE/DRAIN EXTENSIONS

Appl. No.:

09/476,961

Filing

01/03/2000

Date:

Examiner:

Warren, M.

Art Unit:

2815

INTERVIEW SUMMARY

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Sir:

On October 7, 2003, the undersigned discussed the present application with the Examiner via a telephone interview. The parties discussed the rejection of claim 18 under 35 U.S.C. § 103(a) based on Kadosh et. al. (U.S. 5,677,224) in view of Miller (U.S. 5,625,216). The undersigned explained that the combination of references was improper for a number of reasons including because Kadosh teaches away from a drain extension deeper than a source extension and because a combination of the deep drain of Miller with the deep source extension of Kadosh would destroy the purpose and benefits of Kadosh. As noted in the Interview Summary prepared by the Examiner and dated November 6, 2003, the Examiner indicated that "the purpose of Kadosh was to show the benefits of an asymmetrical source/drain region and that Miller showed additional benefits of an

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asymmetrical system in which the drain is deeper that the source." No agreement was reached.

Respectfully submitted,

Date _____

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